



Jpw

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JUL 14 2005

Group Art Unit: 2825  
Examiner: Naum B. Levin

In Re PATENT APPLICATION Of:

Applicant	:	An-Ru Andrew CHENG et al.	)	
Appln. No.	:	10/626,634	)	
Filed	:	July 25, 2003	)	<u>AMENDMENT</u>
For	:	INTEGRATED CIRCUIT CAPABLE OF LOCATING FAILURE PROCESS LAYERS	)	
Attorney Ref.	:	TOP 298	)	

July 14, 2005

**Mail Stop Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This Amendment is timely filed in response to the Examiner's Action mailed April 15, 2005. Please amend the above-referenced application as shown on the following pages.

**Amendments to the Claims** begin on page 2.

*[Handwritten signature]*  
FEE ENCLOSED:\$  
Please charge any further  
fee to our Deposit Account  
No. 18-0002